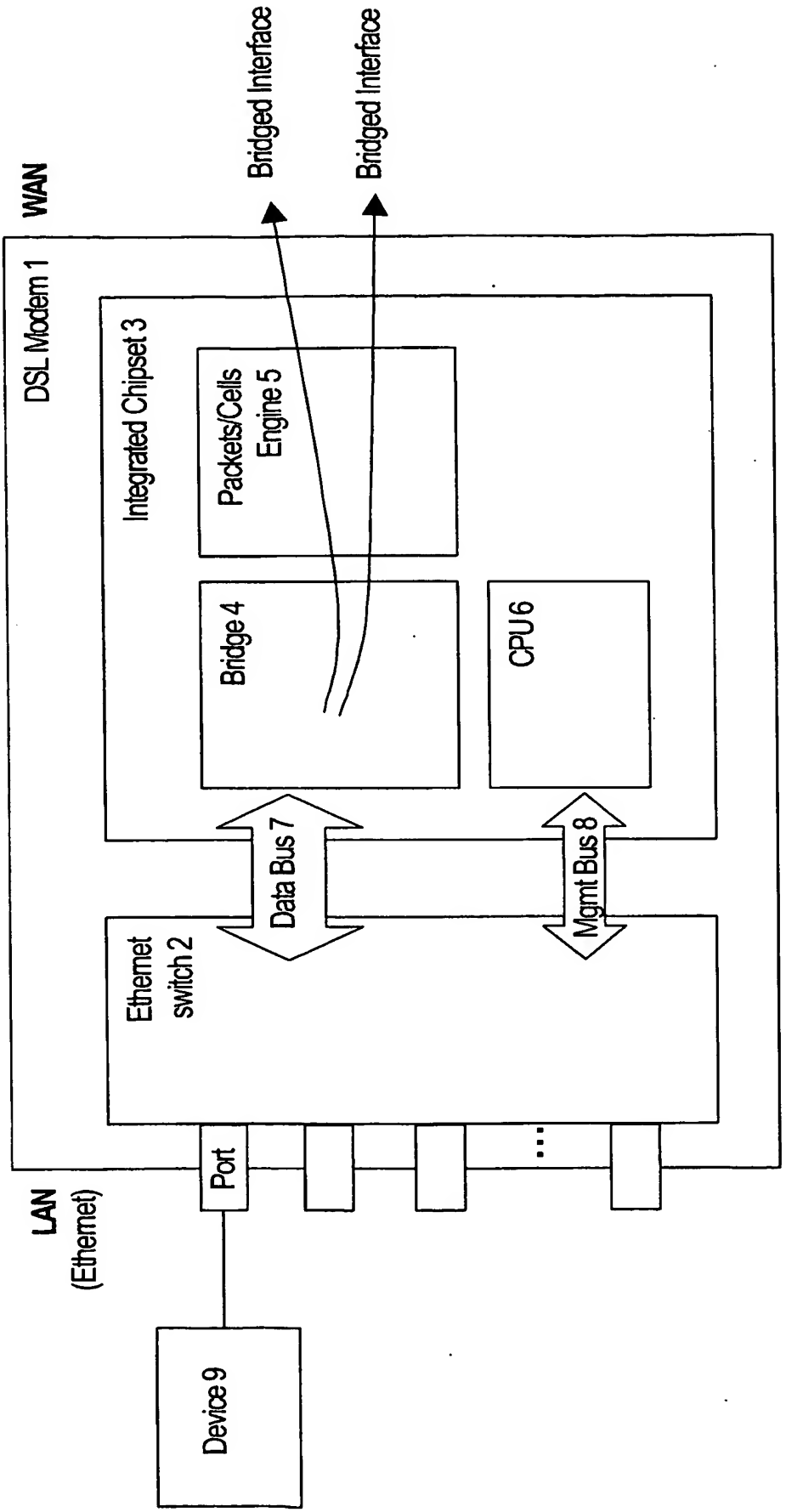
WAN
Bridged Interface
Bridged Interface
DSL Modem 1
Integrated Chipset 3
Packets/Cells Engine 5
Bridge 4
CPU 6
Data Bus 7
Mgmt Bus 8
Ethernet switch 2
Port
LAN
(Ethernet)
Device 9

Fig. 1

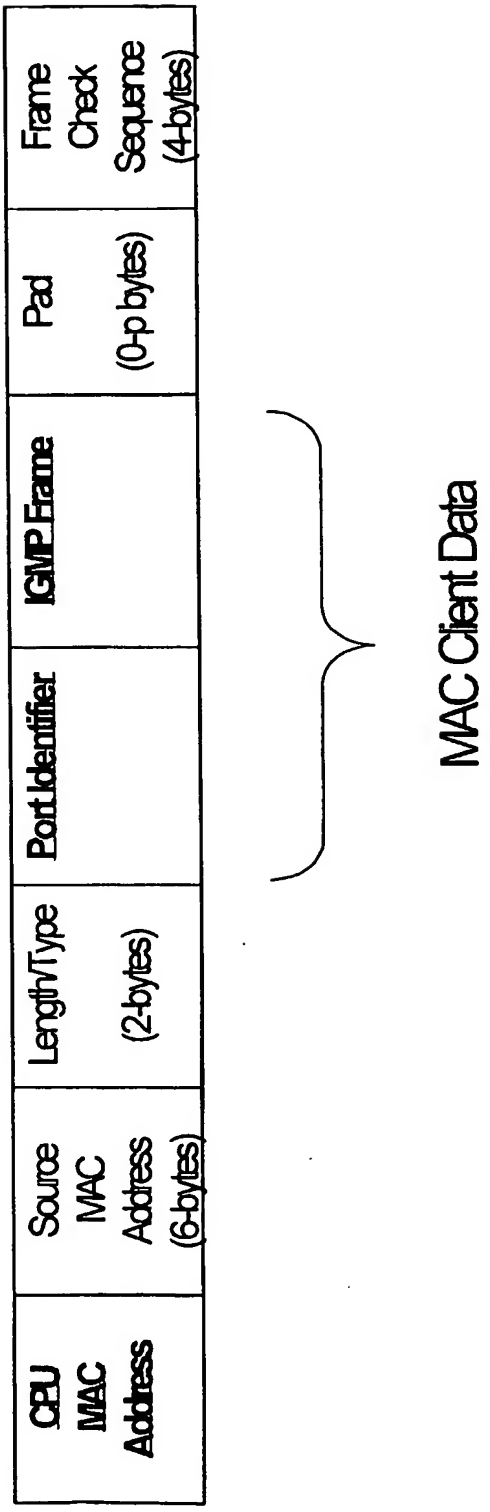
CPU MAC Address
Source MAC Address (6-bytes)
Length/Type (2-bytes)
Port Identifier
IGMP Frame
Pad (0-p bytes)
Frame Check Sequence (4-bytes)
MAC Client Data

Fig. 2

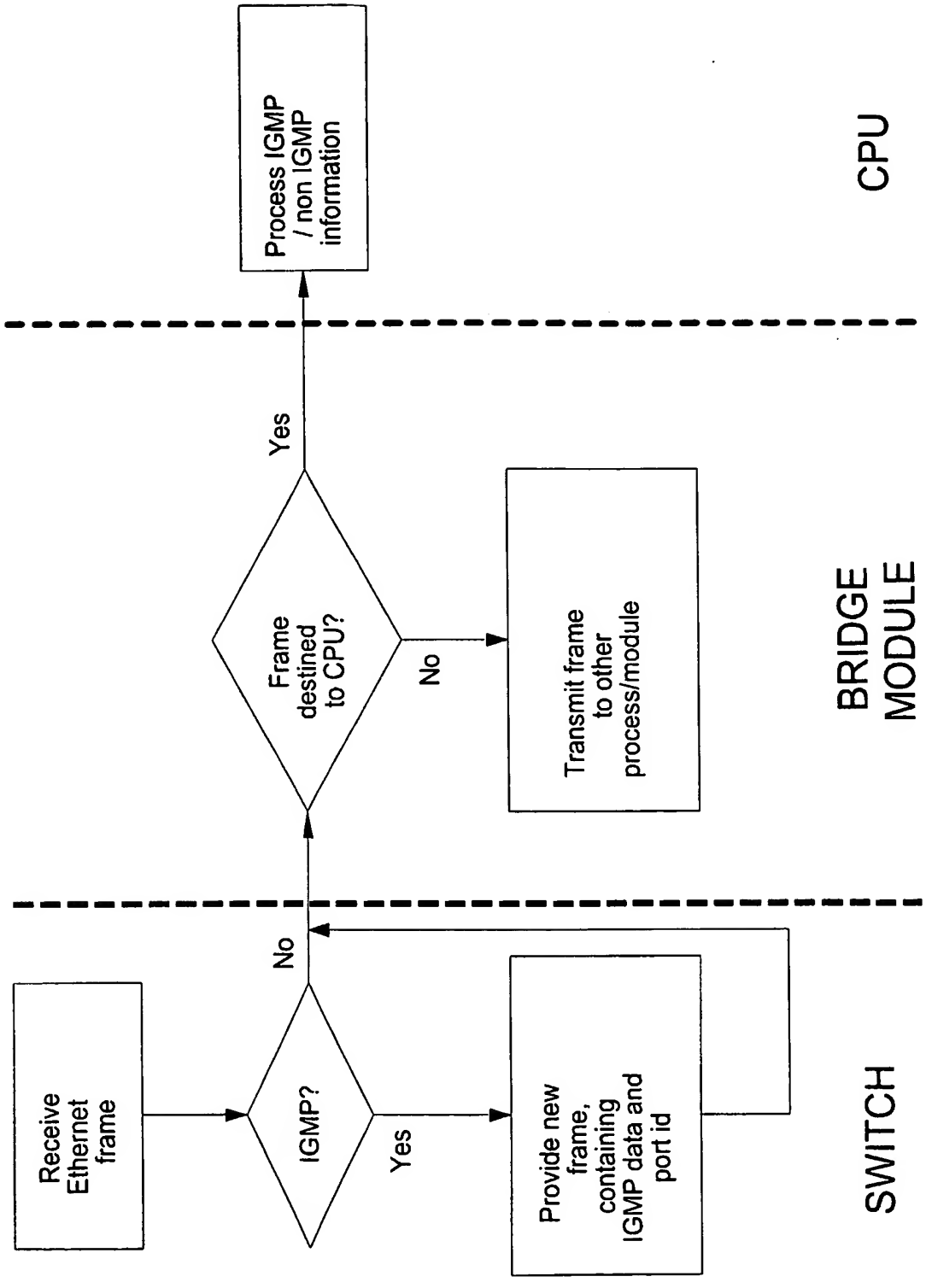
Process IGMP / non IGMP information
CPU
Yes
Frame destined to CPU?
No
Transmit frame to other process/module
BRIDGE MODULE
No
Receive Ethernet frame
IGMP?
Yes
Provide new frame, containing IGMP data and port id
SWITCH

Fig. 3